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L3 ANSWER 1 OF 2 CAPLUS COPYRIGHT 2003 ACS  
AN 2001:12151 CAPLUS  
DN 134:75129  
TI Procedure for selective coating of ceramic surfaces  
IN Roethlingshoefer, Walter; Boehm, Manfred  
PA Robert Bosch G.m.b.H., Germany  
SO Ger. Offen., 6 pp.  
CODEN: GWXXBX  
DT Patent  
LA German  
IC ICM C04B041-84  
CC 57-2 (Ceramics)  
Section cross-reference(s): 76

FAN.CNT 1

	PATENT NO.	KIND	DATE	APPLICATION NO.	DATE
PI	DE 19930782	A1	20010104	DE 1999-19930782	19990703 <--
	JP 2003504846	T2	20030204	JP 2001-508489	20000621
PRAI	DE 1999-19930782	A	19990703		
	WO 2000-DE2023	W	20000621		
AB	Side surfaces of the Si-based ceramic hybrid substrate are esterified by dipping in or spraying with a soln. of a Si-contg. coordinated org. compd. esp. siloxane, and heat treated for 0.4-0.6 h at 1000. The org. soln. contains 0.1-1 vol.-% of siloxane and isopropanol in the balance. The procedure is suitable in the manuf. of switching arrangements consisting of ceramic hybrid substrates and metal elec. conductors.				
ST	coating ceramic surface siloxane hybrid elec switch				
IT	Ceramics				
	(Si-based, coating of; selective coating of ceramic surfaces by esterification with siloxane)				
IT	Polysiloxanes, uses				
	RL: MOA (Modifier or additive use); USES (Uses)				
	(coating with; selective coating of ceramic surfaces by esterification with siloxane)				
IT	Ceramic coatings				
	(esterification with siloxane; selective coating of ceramic surfaces by esterification with siloxane)				
IT	Electric switches				
	(hybrid ceramic-metal; selective coating of ceramic surfaces by esterification with siloxane)				
IT	Integrated circuits				
	(hybrid, coating of ceramic surfaces; selective coating of ceramic surfaces by esterification with siloxane)				
IT	Coating materials				
	(siloxane; selective coating of ceramic surfaces by esterification with siloxane)				

L3 ANSWER 2 OF 2 WPIDS (C) 2003 THOMSON DERWENT  
AN 2001-081893 [10] WPIDS  
DNC C2001-023817  
TI Process for treating the surface of a ceramic-hybrid substrate having ceramic surface regions and metallic surface regions used in the production of electrical circuits comprises esterifying the ceramic surface.  
DC L02 L03  
IN BOEHM, M; ROETHLINGSHOEFER, W  
PA (BOSC) BOSCH GMBH ROBERT  
CYC 21  
PI DE 19930782 A1 20010104 (200110)\* 5p C04B041-84 <--

WO 2001003174 A1 20010111 (200110) DE H01L021-48  
RW: AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE  
W: JP US

EP 1198831 A1 20020424 (200235) DE H01L021-48  
R: AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE  
JP 2003504846 W 20030204 (200320) 13p H05K003-28

ADT DE 19930782 A1 DE 1999-19930782 19990703; WO 2001003174 A1 WO 2000-DE2023  
20000621; EP 1198831 A1 EP 2000-952857 20000621, WO 2000-DE2023 20000621;  
JP 2003504846 W WO 2000-DE2023 20000621, JP 2001-508489 20000621

FDT EP 1198831 A1 Based on WO 200103174; JP 2003504846 W Based on WO 200103174  
PRAI DE 1999-19930782 19990703

IC ICM C04B041-84; H01L021-48; H05K003-28  
ICS C04B041-88; H01L023-12; H01L023-498

AB DE 19930782 A UPAB: 20010220

NOVELTY - Process for treating the surface of a ceramic-hybrid substrate  
having ceramic surface regions and metallic surface regions comprises  
esterifying the ceramic surface.

DETAILED DESCRIPTION - Preferred Features: The ceramic surface  
regions are treated with a solution containing organic components. The  
ceramic structure is based on silicon and the solution is a siloxane  
solution.

USE - Used in the production of electrical circuits, e.g. in a  
vehicle electronic system for controlling the engine and antiblock  
control.

ADVANTAGE - Short circuits between neighboring metallic surface  
regions are avoided.

Dwg.0/5

FS CPI

FA AB

MC CPI: L02-G07; L03-H04E5; L03-J

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